Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3011	native and MOS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:51
L2	1064298	bias\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:51
L3	968	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:04
L4	734360	composite	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:57
L5	968	3 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:57
L6	0	5 not 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:57
L7	1496057	(low adj threshold) or (zero threshold)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:06
L8	1670	7 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:05

L9	4260	((low adj threshold) or (zero adj	LIC DCDLID.	OB	OFF	2005/10/20 16 25
	4200	threshold)) same transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:06
L10	95	9 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:15
L11	7176	9 or 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:16
L12	2021	9 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:16
L13	47059	NMOS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:16
L14	716	12 AND 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:16
L15	674	14 NOT 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:33
L16	5991	3 MPT 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:34

				,		
L17	968	3 NOT 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:34
L18	63	17 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:35
L19	0	17 not 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:35
L20	905	17 not 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:54
L21	. 0	composite same NMOS same ((different adj bias\$4) or (second adj bias\$4) or (plurality adj bias\$4)) same (low\$2 adj threshold).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:09
L22	0	composite same NMOS same (gate near2 (transisor or MOS or NMOS or FET) near2 (coupl\$4 or connect\$4) near2 source) same (low\$2 adj threshold).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:11
L23	0	(source adj follower) same ((second or plurality) adj (NMOS or FET or MOS)) SAME (current adj source) same ((low\$4 adj threshold) or (zero adj threshold)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:05
L24	0	composite same NMOS same ((different adj bias\$4) or (second adj bias\$4) or (plurality adj bias\$4)) same ((low\$2 adj threshold) or (zero adj threshold)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:06

	T					
L25	0	composite same NMOS same (gate near2 (transisor or MOS or NMOS or FET) near2 (coupl\$4 or connect\$4) near2 source) same ((low\$2 adj threshold) or (zero adj threshold)). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:07
L26	0	composite same ((plurality adj NMOS) or (second adj NMOS)) same bias\$4 same ((low\$2 adj threshold) or (zero adj threshold)) same resistor.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:09
L27	0	((plurality adj NMOS) or (second adj NMOS)) same bias\$4 same ((low\$2 adj threshold)) or (zero adj threshold)) same resistor.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:09
L28	3	NMOS same ((different adj bias\$4) or (second adj bias\$4) or (plurality adj bias\$4)) same (low\$2 adj threshold). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 17:10
L29	0	NMOS same ((different adj bias\$4) or (second adj bias\$4) or (plurality adj bias\$4)) same (low\$2 adj threshold). clm.	US-PGPUB	OR	OFF	2005/10/20 17:10
L30	1	NMOS same (gate near2 (transisor or MOS or NMOS or FET) near2 (coupl\$4 or connect\$4) near2 source) same ((low\$2 adj threshold) or (zero adj threshold)).clm.	US-PGPUB	OR	OFF	2005/10/20 17:12